



ATHARVA EDUCATIONAL TRUST'S  
**ATHARVA COLLEGE OF ENGINEERING**

(Approved by AICTE, Recognized by Government of Maharashtra  
& Affiliated to University of Mumbai - Estd. 1999 - 2000)

Department of Electronics & Telecommunication

**Report on**  
One day seminar on "Vulnerability Assessment and Penetration Testing (Awareness of Cyber in Security)"

**Dated :** 8/3/2018

**Organised by :** EXTC Department

**Time :** 9.00am to 11.00am

**Venue :** Presentation Hall, 4<sup>th</sup> Floor, Atharva College of Engineering, Atharva Education complex , Malad Marve Road , Charkop Naka , Malad ( W), Mumbai 400095

**About the Seminar :**

This seminar was conducted by Prof. Dayanand Ambawade which covered the basics of cyber security. One of the most problematic elements of cyber security is the quickly and constantly evolving nature of security risks. The traditional approach has been to focus most resources on the most crucial system components and protect against the biggest known threats, which necessitated leaving some less important system components undefended and some less dangerous risks not protected against. Such an approach is insufficient in the current environment. There is a considerable amount of confusion in the industry regarding the differences between vulnerability scanning and penetration testing, as the two phrases are commonly interchanged. However, their meaning and implications are very different. A vulnerability assessment simply identifies and reports noted vulnerabilities, whereas a penetration test (Pen Test) attempts to exploit the vulnerabilities to determine whether unauthorized access or other malicious activity is possible. Penetration testing typically includes network penetration testing and application security testing as well as controls and processes around the networks and applications, and should occur from both outside the network trying to come in (external testing) and from inside the network.

**No. of participants: 52**

Address : Malad-Marve Road, Charkop Naka, Malad (W), Mumbai 400095, Maharashtra, India

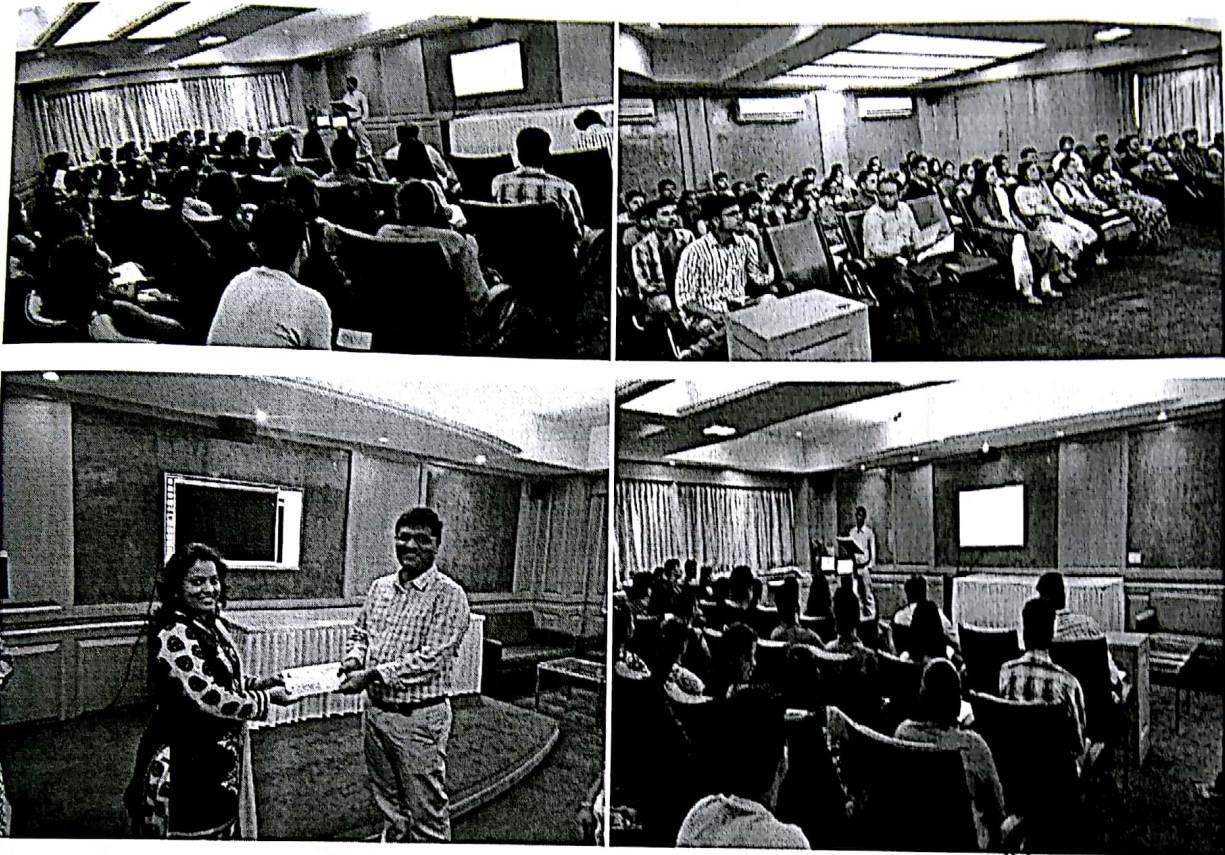




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Department of Electronics & Telecommunication



Workshop Committee  
(EXTC)

Prof. Mahalaxmi Palinje  
Prof. Ritu Sharma

  
H.O.D

Prof. Jyoti Kolap



Address : Malad-Marve Road, Charkop Naka, Malad (W), Mumbai 400095, Maharashtra, India